

Sean MOORMAN

PERSONAL INFORMATION

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ABOUT ME

Description	Highly effective and experienced developer with a knack for maximizing efficacy, learning new skills, and debugging. A demonstrated past of being a difference maker and adapting to the task at hand. Skilled at designing frameworks and building technology that maximizes the efficacy of other developers and users. Experience with recruiting top notch teammates, mentoring new colleagues, and directing development resources to profitable endeavors. Deep knowledge of modern financial markets, distributed computing, and low latency and high throughput data processing.
Objective	Seeking a high impact role to develop technology to revolutionize industries and institutions.

WORK EXPERIENCE

2011-2018	Senior Developer at CITADEL, New York <i>Exchange Traded Funds 2015-2018</i> Built out Citadel's nascent ETF business from a team of 2 traders and 2 developers to 6 traders and 6 developers with coverage of all US traded ETF funds including Domestic, International, Fixed Income and Commodity backed securities. The technology developed was responsible for trading and managing risk for billion dollar notional portfolios. Areas of expertise include Net Asset Value Pricing, Risk Management, Automated Trading, Automated Hedging, Real-time Post Trade Analysis, Real-time Trader Control, Block Pricing and Execution, and Creation/Redemption Management. <i>High Touch Algo Business and Dark Pool 2014-2015</i> Core development of Citadels Algo Engine, Order Gateway, and Dark Pool. Focus on improving client latency and reliability. <i>Core Infrastructure 2011-2014</i> Developed core infrastructure of Citadel's next generation trading architecture. Key contributions include market data replay and real-time risk management infrastructure.
2010	Summer Intern at QUALCOMM, San Diego <i>FPGA emulation</i> Compiled, Emulated, and Debugged next generation processor hardware on FPGAs. The team helped provide a platform for hardware designs to be tested before being fabricated.
2009	Summer Intern at NVIDIA, Santa Clara Tested and debugged recently fabricated chips at temperature and voltage extremes.

EDUCATION

- MAY 2011 M.S. ELECTRICAL AND COMPUTER ENGINEERING
Carnegie Mellon University, Pittsburgh
summa cum laude | Emphasis on Machine Learning and Pattern Recognition
- MAY 2010 B.S. ELECTRICAL AND COMPUTER ENGINEERING
Carnegie Mellon University, Pittsburgh
summa cum laude | Emphasis on Computers Architecture, Software, and Operating Systems

COMPUTER SKILLS

Expert Knowledge: C++, PYTHON, R, SQL, LINUX, ZSHELL, NUMPY, PANDAS, HTML, DJANGO
Basic Knowledge: JAVA, L^AT_EX, REDIS, FLASK, JAVASCRIPT

INTERESTS AND ACTIVITIES

Photography, Videography, Traveling, Hiking, Politics
Strategy Games, Home Automation